

BTCTMiner – Bitcoin Miner for ZTEX FPGA Boards

Bitcoin is a cryptographic currency. BTCTMiner calculates new transaction records for it using ZTEX FPGA Boards. This process is called Bitcoin Mining because it yields new currency.

Features

- Dynamic frequency scaling based on error measurement, i.e. BTCTMiner automatically chooses the frequency with the highest rate of valid hashes
- Cluster mode: one software instance can control many FPGA Boards (up to several hundred, limited by the number of USB host controllers)
- Hot-Plugging: devices can be added / removed during runtime
- Stale reduction by Long Polling and block monitoring
- Support of backup server / pools
- Package is Open Source
- Ready-to-use Bitstream (no Xilinx Software or Licence required)
- Known to work under Linux and Windows

Resources

For more information please visit:

- BTCTMiner home page: <http://www.ztex.de/btctminer>
- Bitcoin home page: <http://www.bitcoin.org>
- Bitcoin Wiki: <https://bitcoin.it>
- ZTEX FPGA Boards: <http://www.ztex.de>

Supported FPGA Boards

FPGA Board	FPGA	Hashes per clock	Minimum hash rate	Typical hash rate	Power @ 12V
USB-FPGA Module 1.15b	XC6SLX75-3	0.5	84 MH/s	90MH/s	5.0W @ 184 MHz
USB-FPGA Module 1.15d	XC6SLX150-3	1	184 MH/s	200MH/s	8.9W @ 200 MHz
USB-FPGA Module 1.15x	XC6SLX150-N3	1	184 MH/s	200MH/s	8.9W @ 200 MHz