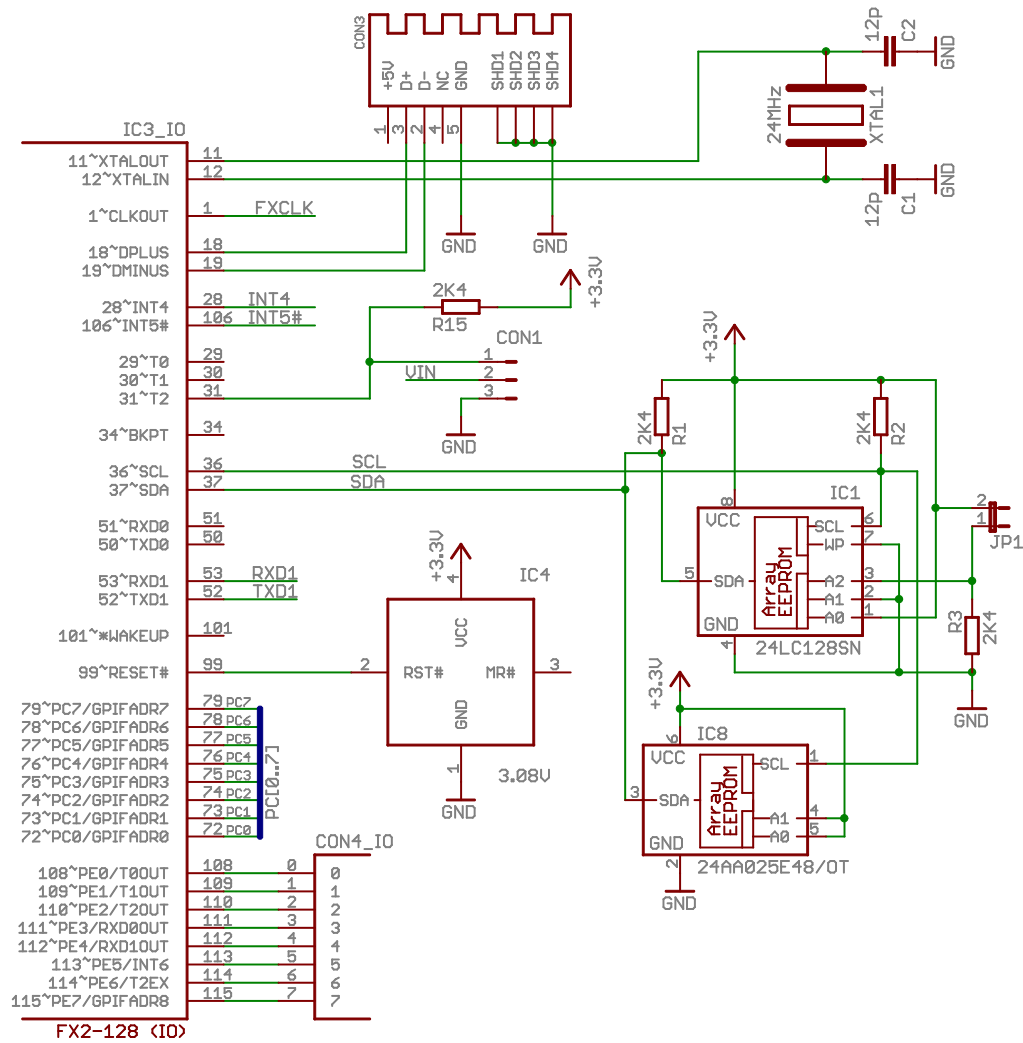
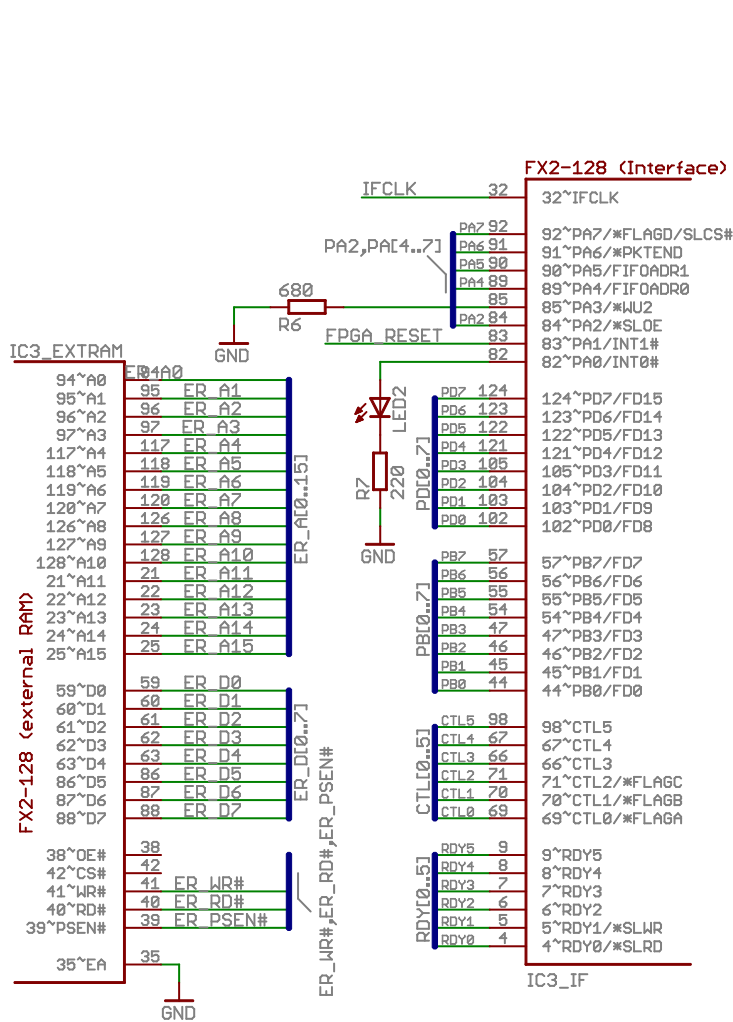
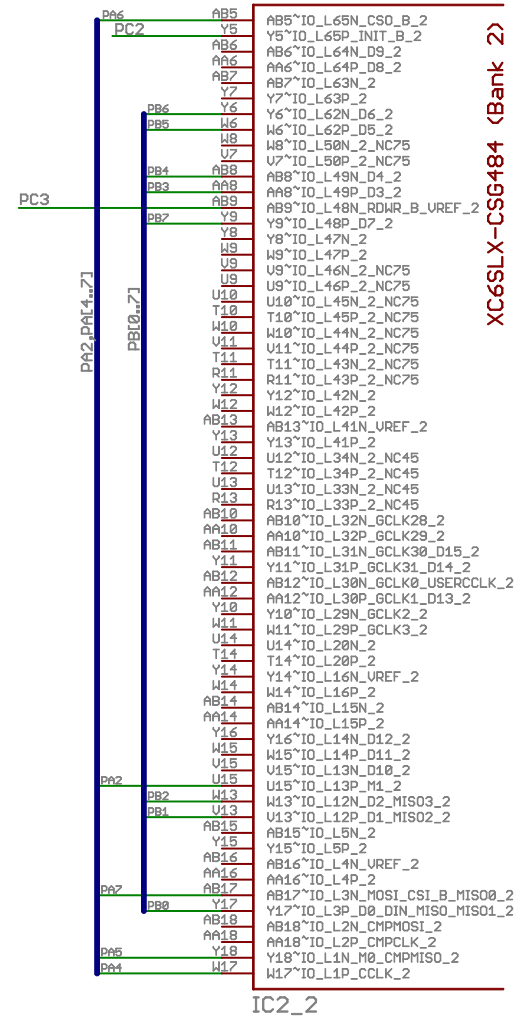
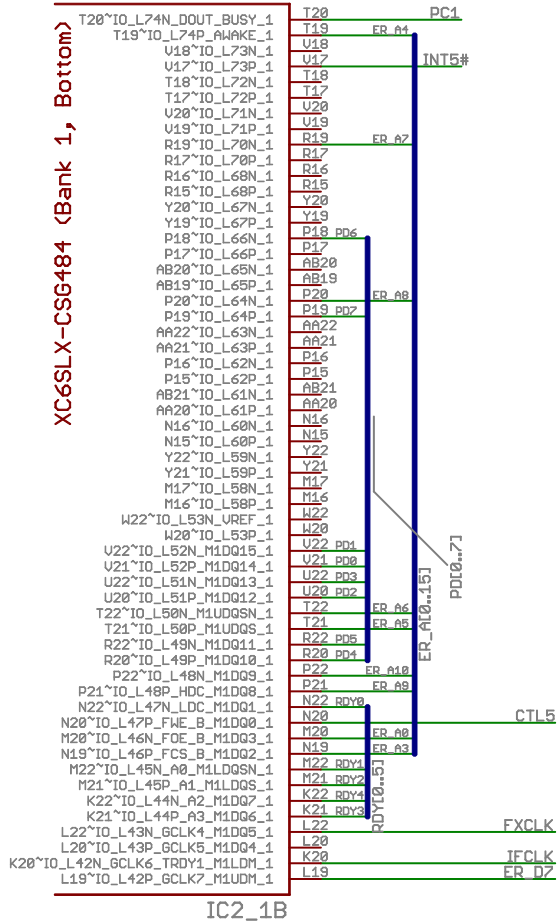
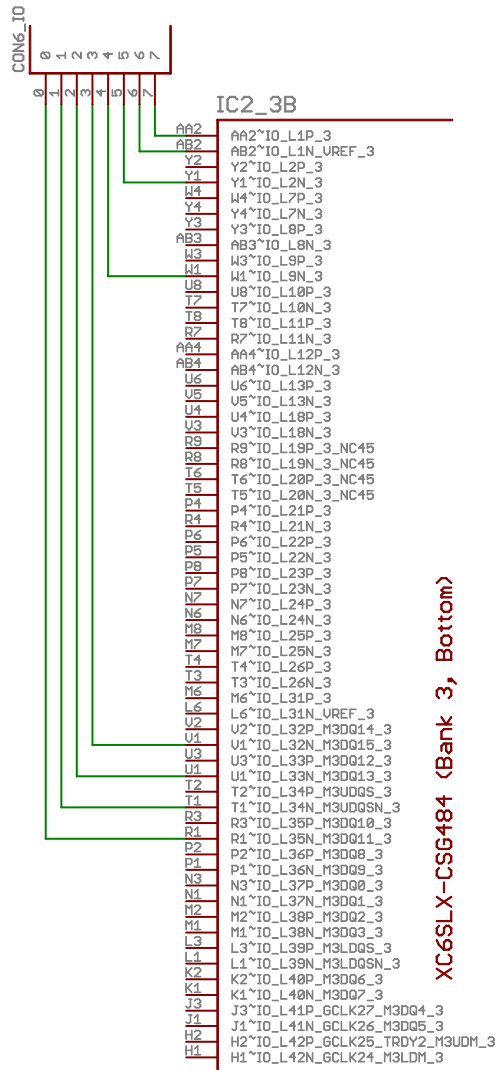


Circuit diagram of ZTEX USB-FPGA-Module 1.15x

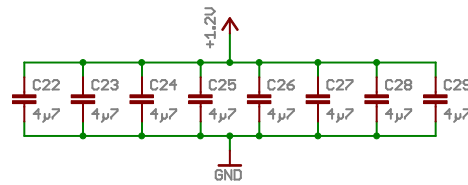
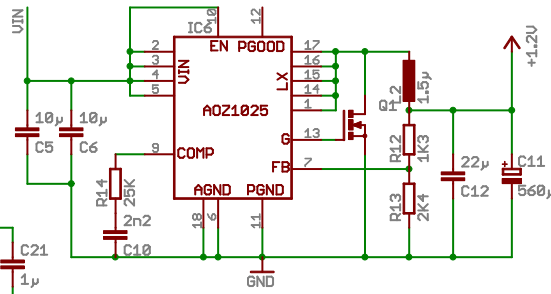
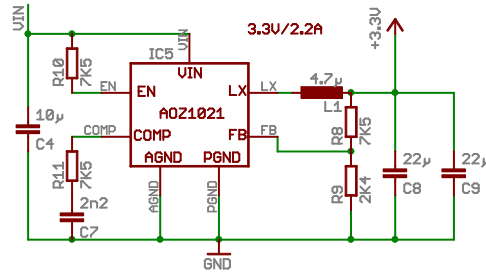
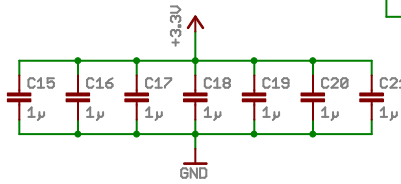
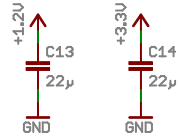
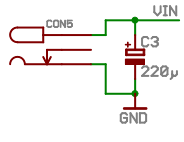
Contents:

Sheet 1	EZ-USB FX2 I/O, USB, EEPROM
Sheet 2	JTAG, XC6S Bank 1T
Sheet 3	XC6S Banks 1B, 2 and 3
Sheet 4	Power





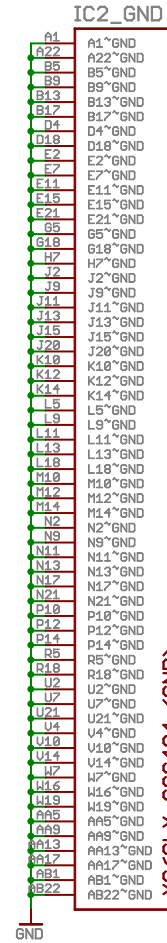
Input voltages 4.5..16V



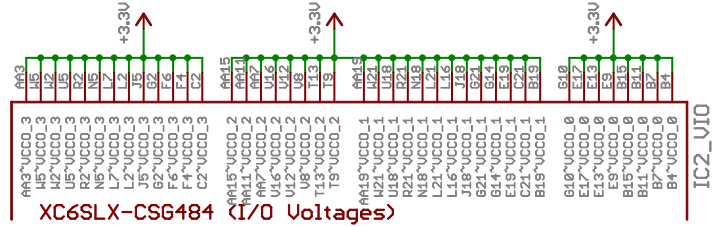
Capacitor selection rules:

22µF: low ESR, X5R or X7R

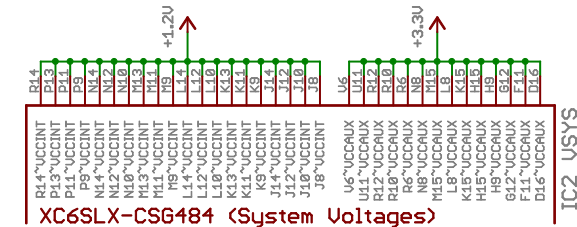
4.7µF: $Z < 0.10\Omega$ @ 0.5..100MHz, $Z < 0.40\Omega$ @ 500MHz



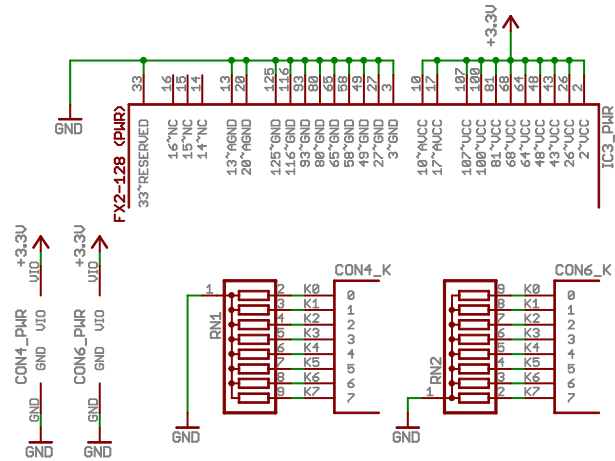
XC6SLX-CSG484 <GND>



XC6SLX-CSG484 <I/O Voltages>



XC6SLX-CSG484 <System Voltages>



XC6SLX-CSG484 <GND>